Specification of Competency Standards for the Information & Communications Technology Industry Unit of Competency

Functional Area - Architecture

Title	Develop the microservices architecture
Code	111128L5
Range	Develop a microservices architecture reference model from various viewpoints to align with the business requirements as well as requirements from higher level architecture
Level	5
Credit	3 (For Reference Only)
Competency	Performance Requirements 1. Review the benefits and challenges of a microservices architecture • Be able to o understand the benefit of decomposing an application into microservices, such as modularity, scalability, integration, etc understand the protocols that microservices communicate with each other • Synchronous • Asynchronous • UI integration address the challenges of embarking on a microservices architecture • Complexity • Lack of governance • Latency • Data integrity • Versioning 2. Understand various microservices architecture reference models • Be able to o understand the development processes for building a microservices architecture identify the boundaries of the microservices o understand the strategy of decomposing and decoupling a monolithic application into a series of microservices 3. Develop the microservices architecture by evaluating business capabilities, and the software and hardware environment. • Be able to o choose suitable hosting model for the computing resources depict the microservices that enable reasoning about critical requirements and constrains all subsequent refinements. o conduct the evaluation and perform the analysis of the microservices
Assessment Criteria	The integrated outcome requirements of this UoC is the ability to develop a microservice architecture in alignment with the business capabilities, and software and hardware environment as well as the business requirements from higher level architecture
Remark	